

PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**Title:** "Non-Volatile Multi-Stable Memory Device  
and Methods of Making and Using the Same"

**Applicants:** Luisa Dominica Bozano et al.  
**Attorney Docket No.:** ARC920030037US1

<b>Serial No.:</b> 10/	<b>Examiner:</b>
<b>Filed:</b> August 20, 2003	<b>Art Unit:</b>

Mail Stop: Patent Application  
Commissioner for Patents  
P.O.Box 1450  
Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT**  
**WITHIN THREE MONTHS OF FILING OR BEFORE**  
**MAILING OF FIRST OFFICE ACTION (37 CFR 1.97(b))**

Sir:

Applicants submit herewith Form PTO-1449A listing documents believed relevant to the subject application. It is respectfully requested that these documents be made of record and an initialed copy of Form PTO-1449A be returned to the undersigned.

This Disclosure Submission Statement should not to be construed as a representation that a search has been made, that additional material to the examination of this application does not exist, or that the citations constitute prior art.

It is believed that this Disclosure Submission Statement complies with the requirements of 37 CFR 1.56, 1.97, and 1.98, and the Manual of Patent Examining Procedures, Sections 609 and 707.05. If for any reason the Examiner considers otherwise, it is respectfully requested that the undersigned be called so that any deficiencies can be remedied.

A copy of each document is enclosed unless indicated otherwise. Some of the documents may have markings on them. No significance is meant to be attached to the markings.

Respectfully submitted,

Date: August 20, 2003



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Attachment

<b>Form PTO-1449 (modified)</b>		<b>Attorney Docket No.</b> <b>ARC920030037US1</b>	<b>Serial No.</b>
<b>LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT(S)' INFORMATION DISCLOSURE STATEMENT</b>		<b>Applicants:</b> <b>Luisa Dominica Bozano et al.</b>	
(Use several sheets if necessary)		<b>Filing Date:</b> <b>08/20/2003</b>	<b>Group Art Unit:</b>

**U.S. PATENT DOCUMENTS**

Examiner Initials	Document No.	Date (MM.DD.YY)	Name	Class / Subclass	Filing Date
	6,055,180	04.25.2000	Gudesen et al.	365/175	02.12.1999
	6,128,214	10.03.2000	Kuekes et al.	365/151	03.29.1999
	2002/0066933 A1	06.06.2002	Tsu-Jae	257/410	12.21.2001
	6,531,735B1	03.11.2003	Kamigaki et al	257/324	09.13.2000
	4,717,943	01.05.1988	Wolf et al.	357/23.5	07.16.1986
	4,503,521	03.05.1985	Schick et al.	365/182	06.25.1982
	4,242,697	12.30.1980	Berthold et al.	357/49	03.14.1979
	2003/0015752 A1	01.23.2003	Palm et al.	257/315	08.09.2001
	6,504,755B1	01.07.2003	Katayama et al.	365/185.15	11.08.2001
	6,194,759B1	02.27.2001	Sano et al.	257/314	11.09.1999
	4,384,299	05.17.1983	Raffel et al.	357/23	01.04.1982
	5,306,586	04.26.1994	Pai et al.	430/58	08.06.1992
	4,110,839	08.29.1978	Bert et al.	365/180	09.20.1977

**Examiner**

**Date Considered**

**Examiner:** Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).

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<i>(Use several sheets if necessary)</i>	<b>Filing Date:</b> <b>08/20/2003</b>	<b>Group Art Unit:</b>

## **FOREIGN PATENT DOCUMENTS**

Examiner Initials	Document No.	Publication Date MM/DD/YYYY	Country	Int'l Class Subclass	Translation Yes      No
	JP 2001156275A (Front page with English Abstract)	06.08.2001	Japan	G11C16/04	No
	WO 02/37500A1	05.24.2001	WIPO/PCT	G11C11/36	No

## **PUBLICATIONS / OTHER ART**

Examiner Initials	Citation
	Liping Ma et al., "Nonvolatile electrical bistability of organic/metal-nanocluster/organic system," Applied Physics Letters, volume 82, number 9, March 3, 2003.
	Ju Krieger et al., "Molecular analogue memory cell based on electrical switching and memory in molecular thin films," Synthetic Metals 122 (2001), pp 199-202.
	Liping Ma et al. , "Organic bistable light-emitting devices," Applied Physics Letters, Volume 80, number 3, January 21, 2002.
	J. Simmons et al., "New conduction and reversible memory phenomena in thin insulating films," Proc. Roy. Soc. A. 301, 77-102 (1967).
	P. Arnett. "FERROELECTRIC FET DEVICE," IBM Technical Disclosure Bulletin Vol. 15 No. 9 February 1973.

## **Examiner**

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